

TECHNIQUE FOR OPTICALLY CONVERTING WAVELENGTHS
IN A MULTI-WAVELENGTH SYSTEM

CROSS-REFERENCE TO RELATED APPLICATIONS

5 This patent application is related to U.S. Patent
Application No. _____ (Attorney Docket No. 57983-
000015, Client Reference No. 12922RO), filed concurrently
with this patent application, and which is hereby
incorporated by reference herein in its entirety.

10 This patent application is also related to U.S.
Patent Application No. _____ (Attorney Docket No.
57983-000018, Client Reference No. 12946RO), filed
concurrently with this patent application, and which is
hereby incorporated by reference herein in its entirety.

FIELD OF THE INVENTION

15 The present invention relates generally to optical
wavelength conversion and, more particularly, to a
technique for optically converting wavelengths in a
20 multi-wavelength system.

BACKGROUND OF THE INVENTION

25 All-optical wavelength conversion is an important
feature of multi-wavelength optical systems such as
wavelength-switching networks. Solutions to provide all-
optical wavelength conversion have been studied to a
great extent in the context of wavelength-switching,
because they simplify network management, and provide
superior blocking performance (see B. Ramamurthy and B.
30 Mukherjee, "Wavelength-conversion in WDM networking",
IEEE Journal on Selected Areas on Communications, vol.
16, pages 1061-1073, September 1998). These solutions

comprise hardware designs for elementary converters (see
S. Yoo, "Wavelength-conversion technologies for WDM
network applications", IEEE Journal of Lightwave
Technology, vol. 14, pages 955-966, June 1996; J.
5 Elmirghani and H. Mouftah, "All-optical wavelength
conversion: technologies and applications in DWDM
networks", IEEE Communications Magazine, pages 86-92,
March 2000), as well as techniques to make the best use
of limited wavelength conversion resources. Several
10 hardware designs are possible for all-optical wavelength
conversion, which include cross-gain or cross-phase
modulation in semiconductor optical amplifiers, as well
as wave-mixing techniques based on nonlinear media. The
devices resulting from these different techniques have
15 diverging characteristics in terms of their transparency,
their bandwidth, and their bulk wavelength conversion
capability. For example, devices based on cross-gain
modulation provide limited signal transparency and have
no bulk wavelength conversion ability, as they only
20 accept one input signal at any time. On the other hand,
these devices have a mature manufacturing process. They
have been commercially available for several years. Wave-
mixing converters are more recent but offer many
advantages, such as a high signal transparency and bulk
25 wavelength conversion capabilities. Yet their
manufacturing processes are still immature. In general,
all-optical wavelength converters remain expensive.
Therefore, we need to minimize requirements for such
devices in any multi-wavelength system.

30 The above mentioned evolution has constrained
previous solutions to using converters with no bulk
wavelength conversion capacity, like the ones based on
cross-gain modulation. Converters with no bulk wavelength

conversion capability, which are also called single-input converters, offer few options to provide wavelength conversions in multi-wavelength systems. The solutions are limited to mapping each input frequency to its image
5 by an atomic wavelength conversion, which is implemented with dedicated converters. This technique enables the building of strictly non-blocking multi-wavelength optical cross-connects (see B. Ramamurthy et al. referenced above). However, it produces high converter
10 costs, as the number of all-optical converters is $O(FW)$, where W is the number of wavelengths and F is the number of fibers.

The development of wave-mixing converters has motivated new techniques for wavelength conversion. Some
15 of these solutions reduce converter requirements by exploiting bulk wavelength conversion inherent in wave-mixing (see N. Antoniadou, S. Yoo, K. Bala, G. Ellinas, and T. Stern, "An architecture for a wavelength-interchanging cross-connect utilizing parametric
20 wavelength-converters", IEEE Journal of Lightwave Technology, vol. 17, pages 113-1125, July 1999). In such architectures, input frequencies are usually converted to their image, through a cascade of elementary wavelength conversions. These conversions follow parametric
25 relationships characterizing the type of wave-mixing converter used (see S. Yoo referenced above). For example, in the case of converters based on difference-frequency generation, each input frequency f is mapped to $f^p - f$, where f^p is the pump frequency of the converter.
30 For example, rearrangeable wavelength-interchanging cross-connects have been proposed that are based on a modified Benes interconnection topology (see N.

Antoniades et al. referenced above). However, these techniques do not lead to any dramatic reduction of converter requirements. Indeed, the most efficient technique described so far still uses a number of wave-
5 mixing converters, of the order of the number of wavelengths, per fiber (see N. Antoniades et al. referenced above). Most previous work focuses on the design of all-optical wavelength switches that have the capability to provide dynamic mappings between incoming
10 wavelengths and outgoing wavelengths. Yet few studies consider the problem of the all-optical implementation of static frequency mappings. Such mappings have an important role in all-optical signal processing.

In view of the foregoing, it would be desirable to
15 provide a technique for optically converting wavelengths in a multi-wavelength system in an efficient and cost effective manner which overcomes the above-described inadequacies and shortcomings.

20 SUMMARY OF THE INVENTION

According to the present invention, a technique for optically converting wavelengths in a multi-wavelength system is provided. In one embodiment, wherein the multi-wavelength system has W wavelength channels, wherein $W =$
25 2^N , the technique is realized by selectively directing a received frequency channel corresponding to a respective wavelength channel based upon a predetermined frequency mapping. Then, the frequency of the selectively directed frequency channel is shifted at least once by an amount
30 defined by $\pm 2^i \Delta f$, wherein Δf is the frequency spacing between adjacent frequency channels, and $i = 0, 1, \dots, N-1$.

In accordance with other aspects of the present invention, wavelength channel ordering may be preserved by only shifting the frequency of the selectively directed frequency channel to a higher frequency.

5 Moreover, the shifting of the frequency of the selectively directed frequency channel may be constrained such that the frequency of the selectively directed frequency channel is shifted at least once by an amount defined by $+2^{N-1-i}\Delta f$. Alternatively, the shifting of the
10 frequency of the selectively directed frequency channel may be further constrained such that the frequency of the selectively directed frequency channel is shifted at least once by an amount defined by $2^{N-1-\lfloor \log_2 \kappa \rfloor - i}$, wherein κ is an integer and $i=0, \dots, N-1-\lfloor \log_2 \kappa \rfloor$. It is noted that the
15 amount by which the frequency of the selectively directed frequency channel is shifted beneficially decreases as the number of times the frequency of the selectively directed frequency channel is shifted increases.

In accordance with further aspects of the present
20 invention, wavelength channel ordering may be preserved by only shifting the frequency of the selectively directed frequency channel to a lower frequency. Moreover, the shifting of the frequency of the selectively directed frequency channel may be constrained
25 such that the frequency of the selectively directed frequency channel is shifted at least once by an amount defined by $-2^i\Delta f$. Alternatively, the shifting of the frequency of the selectively directed frequency channel may be further constrained such that the frequency of the
30 selectively directed frequency channel is shifted at least once by an amount defined by $-2^i\kappa\Delta f$, wherein κ is an integer and $i=0, \dots, N-1-\lfloor \log_2 \kappa \rfloor$. Again, it is noted that the

amount by which the frequency of the selectively directed frequency channel is shifted beneficially decreases as the number of times the frequency of the selectively directed frequency channel is shifted increases.

5 The present invention will now be described in more detail with reference to exemplary embodiments thereof as shown in the appended drawings. While the present invention is described below with reference to preferred
10 embodiments, it should be understood that the present invention is not limited thereto. Those of ordinary skill in the art having access to the teachings herein will recognize additional implementations, modifications, and embodiments, as well as other fields of use, which are within the scope of the present invention as disclosed
15 and claimed herein, and with respect to which the present invention could be of significant utility.

BRIEF DESCRIPTION OF THE DRAWINGS

20 In order to facilitate a fuller understanding of the present invention, reference is now made to the appended drawings. These drawings should not be construed as limiting the present invention, but are intended to be exemplary only.

25 Figure 1 shows an exemplary prior art optical-gating wavelength converter.

 Figure 2 shows a prior art wave-mixing wavelength converter based on difference frequency generation.

30 Figure 3 shows a prior art elementary converter wherein frequency translation is implemented with two cascaded difference frequency wave-mixing devices.

 Figure 4 shows an all-optical pseudo frequency multiplier built with dedicated wavelength converters in

Figure 5 shows an all-optical pseudo frequency divider built with dedicated wavelength converters in accordance with the present invention.

Figure 7 shows a routing and wavelength assignment solution with collision for the logarithmic multistage wavelength converter of Figure 6 in accordance with the present invention.

Figure 8 shows a collision free routing and wavelength assignment solution for the logarithmic multistage wavelength converter of Figure 6 in accordance with the present invention.

Figure 9 shows the general architecture of a priority log-converter in accordance with the present invention.

Figure 10 shows the internal structure of stage i in the priority log-converter of Figure 9 for a constrained increasing up-converter in accordance with the present invention.

Figure 11 shows the frequency assignments at the consecutive stages of the increasing up-converter shown in Figures 9 and 10 in accordance with the present invention.

Figure 12 shows an alternative internal structure of stage i in the priority log-converter of Figure 9 for a constrained increasing up-converter in accordance with the present invention.

Figure 13 shows the frequency assignments at the consecutive stages of the increasing up-converter shown in Figures 9 and 12 in accordance with the present

invention.

Figure 14 shows the internal structure of stage i in the priority log-converter of Figure 9 for a constrained increasing down-converter in accordance with the present invention.

Figure 15 shows the frequency assignments at the consecutive stages of the increasing down-converter shown in Figures 9 and 14 in accordance with the present invention.

Figure 16 shows an alternative internal structure of stage i in the priority log-converter of Figure 9 for a constrained increasing down-converter in accordance with the present invention.

Figure 17 shows the frequency assignments at the consecutive stages of the increasing down-converter shown in Figures 9 and 16 in accordance with the present invention.

Figure 18 shows a pseudo frequency doubler for 4 frequencies constructed in accordance with the present invention design for increasing up-converters.

Figure 19 shows a pseudo frequency divider for 4 frequencies constructed in accordance with the present invention design for increasing down-converters.

DETAILED DESCRIPTION OF EXEMPLARY EMBODIMENT(S)

The present invention provides cost effective techniques to implement static frequency-mappings, in an all-optical manner, with wave-mixing converters. The main idea behind the present invention is to decompose the mapping of each frequency f to its image $\Gamma(f)$, into a cascade of elementary frequency translations by amounts of the form $\pm 2^i \Delta f$, where Δf is the frequency spacing

between adjacent frequency channels. In other words, for some frequency f and the binary representation of $\frac{1}{\Delta f}(\Gamma(f)-f)$ given by $\left(\sum_{l=0}^{N-1} 2^l b_l\right)$, where $b_l \in \{0,1\}$, the present invention comprises converting f into $\Gamma(f)$ with a sequence of frequency translations by $2^l \Delta f$, where $l=0,1,\dots,N-1$. In a system with $W=2^N$ frequencies of the form $f_i=f_0+i\Delta f$, having the constraint that each input frequency is mapped to some other frequency of the available spectrum, the present invention approach reduces the required number of wave-mixing converters to $O(\log_2 W)$. This roughly corresponds to one $\pm 2^i \Delta f$ frequency shifter for each value of i , where $i=0,1,\dots,N-1$. To implement this approach in the most general way, a generic logarithmic converter design, or generic log-converter, is disclosed where a central wavelength-selective cross-connect connects peripheral frequency translation modules, and the size of the central cross-connect is of the order of $O(W(\log_2 W)^2 \log(W(\log_2 W)^2))$. The advantage of generic logarithmic converters is to allow individual frequencies to visit the different frequency translation modules in any arbitrary order. In spite of the flexibility that they offer, the spatial complexity of generic logarithmic converters may be high due to the presence of the central wavelength-selective cross-connect. Indeed, the spatial complexity of the wavelength-selective cross-connect is at least $O(W(\log_2 W)^2 \log(W(\log_2 W)^2))$, and it becomes quite large when the number of frequency channels increases. For this reason, the present invention also discloses another form of logarithmic converters, with a linear multi-stage structure, which are called priority-based logarithmic

converters, or priority log-converters. In these converters, each stage contains two paths. The first path leads to a wave-mixing frequency shifter that provides a discrete frequency translation by $\pm 2\Delta f$ to each input
5 frequency into the stage, while the other path bypasses the frequency shifter and leads directly to the next stage.

The number of stages of a priority log-converter is $O(\log_2 W)$. Priority log-converters owe their names to the
10 fact each input frequency visits the frequency shifting modules in some predetermined order. Priority log-converters are interesting because they can be implemented with a smaller spatial complexity than other solutions, which are based on a central wavelength-
15 selective cross-connect. However, these multi-stage converters offer less flexibility for wavelength assignments than generic log-converters do. An interesting result is that in spite of their constrained design, priority log-converters perform as well as
20 generic log-converters for certain important mappings. Regardless of the type of log-converter, a common requirement to use these converters is to ensure that no frequency collision occurs within any frequency translation module (i.e., the frequencies assigned to any
25 frequency shifting module must always be distinct). The problem is a particular instance of the widely studied routing and wavelength assignment problem (see B. Ramamurthy et al. referenced above). In wavelength-switched networks, the general routing and wavelength
30 assignment problem consists of selecting routes and assigning wavelengths on the selected routes to meet a given traffic demand, while avoiding any frequency

collision on any network link, and while taking into account wavelength continuity constraints. In its general form, the routing and wavelength assignment problem is difficult (see B. Ramamurthy et al. referenced above). In the case of a log-converter, the problem reduces to finding the right order to assign the different input frequencies to the available frequency shifters. In spite of this simplification, the problem remains fairly open, even when there is no additional constraint on the frequency mapping. However, the present invention involves identifying specific frequency mappings, with special properties that enable simple solutions to be found.

Interesting frequency mappings are pseudo frequency-multiplications and pseudo frequency-divisions. A pseudo frequency multiplication or division converts any frequency $f_i = f_0 + i\Delta f$ to $f_{k,i} = f_0 + k.i\Delta f$, where k is the scaling factor of the mapping. The factor k is larger than 1 in the case of a pseudo frequency multiplication, but it is smaller than 1 in the case of a pseudo frequency division. For example, in the case of a dedicated converter solution for an all-optical pseudo frequency multiplier by k , as many as W/k such converters are needed. Fortunately, in addition to their monotonicity properties, pseudo frequency multiplications or divisions satisfy another interesting property. The property is that for any pseudo frequency multiplication (or division) Γ , the mapping $\Gamma'(f) = \Gamma(f) - f$ (or $\Gamma'(f) = f - \Gamma(f)$) is increasing. All these special properties enable the routing and wavelength assignment problem to be efficiently solved.

To provide a broad treatment of the problem, a general class of frequency mappings are introduced, which are called constrained increasing frequency mappings. This class includes pseudo frequency multipliers or
5 dividers. Briefly, a constrained increasing frequency mapping Γ preserves the frequency ordering of the input channels, and is such that the mapping $\Gamma'(f)=|\Gamma(f)-f|$ is also increasing. In the following discussions, two basic types of constrained increasing converters are identified as
10 follows:

- 1.) Constrained increasing up-converter: when each input frequency is smaller than its image; and
- 2.) Constrained increasing down-converter: when each input frequency is larger than its image.

15 The present invention provides a design based on priority log-converters, where the assignments of precedence to the different frequency translation modules is appropriate. The key idea is to assign the precedence in the same order as the magnitude of frequency shifts of
20 the different modules. Independently of the monotonicity properties of a given mapping Γ , it is possible to further reduce converter requirements when the mapping $\Gamma'(f)=|\Gamma(f)-f|$ is some multiple of $\kappa\Delta f$ for all frequencies in the input frequency set (i.e., the set of frequencies to
25 be converted by the constrained increasing mapping), where κ is some integer constant characteristic of the mapping.

I. Elementary Wavelength Converters

30 There are two major types of wavelength converters. The first type is based on optical gating, while the second type is based on wave-mixing in nonlinear media

(see S. Yoo referenced above).

A. Optical Gating Converters

Optical-gating converters include converters using
5 cross-gain modulation in semiconductor optical amplifiers
(see S. Yoo referenced above). They operate by
translating signals carried on some input frequency to
another frequency, in the saturation regime of
semiconductor optical amplifiers. These converters can
10 only convert one input frequency at a time. However, they
can map different input frequencies to the same frequency
(frequency of the pump). Referring to Figure 1, there is
shown an exemplary optical-gating wavelength converter 10
for translating an input signal carried on an input
15 frequency, f_{in} , to an output signal carried on a pump
frequency $f_{out} = f^p$.

B. Wave-Mixing Converters

Wave-mixing converters exploit nonlinear effects in
20 appropriate medias such as optical fibers or
semiconductor optical amplifiers. A variety of nonlinear
effects exist including difference frequency generation
and four wave-mixing (see S. Yoo referenced above).
Converters of this type usually have a precise parametric
25 relationship between the incoming frequencies, the pump
and the outgoing frequencies. In the case of difference
frequency generation with some pump frequency f^p , an
input frequency f is mapped to the output frequency $f^p - f$
(see Figure 2, which shows a wave-mixing wavelength
30 converter 20 based on difference frequency generation).
Major advantages of wave-mixing converters are their high
level of transparency and their ability to simultaneously

convert several input frequencies.

C. Elementary Converters

For purposes of this detailed description, an
5 elementary converter may be any device that falls within
one of the categories described below:

- 1.) The device maps any input frequency f^* to $f^p - f$,
where f^p is some pump frequency, and it can accept
several distinct input frequencies;
- 10 2.) The device maps any input frequency f to $f + \Delta$, where
 Δ is some frequency shift characteristic of the device;
or
- 3.) The device maps any input frequency f to f^p , where
 f^p is some pump frequency.

15 Referring to Figure 3, there is shown an elementary
converter 300 of the second category above wherein
frequency translation is implemented with two cascaded
difference frequency wave-mixing devices. That is,
elementary converter 300 comprises two cascaded
20 difference frequency wave-mixers 303 and 306 driven by
different pump frequencies 302 and 304. Outgoing
frequencies 307 are the result of the translation of
incoming frequencies 301 by an amount equal to the
difference of the pump frequencies in the second and the
25 first wave-mixing devices.

II. Background on Mappings

For purposes of this detailed description, a few
basic definitions about mappings in general are provided.

30 A mapping Γ is a relationship between elements of an
input set Φ_{in} and elements of an output set Φ_{out} . It

associates each element x of Φ_{in} to one and only one element $y=\Gamma(x)$ of Φ_{out} . Assuming that the input and output sets contain real numbers, the mapping Γ is non-decreasing (or non-increasing) if for any elements x_1 and x_2 of Φ_{in} , such that $x_1 \leq x_2$, we have $\Gamma(x_1) \leq \Gamma(x_2)$ (or $\Gamma(x_1) \geq \Gamma(x_2)$). The mapping Γ is increasing (or decreasing) if for any elements x_1 and x_2 of Φ_{in} , such that $x_1 < x_2$, we have $\Gamma(x_1) < \Gamma(x_2)$ (or $\Gamma(x_1) > \Gamma(x_2)$).

10 III. Previous Designs for Pseudo-Frequency Multipliers or Dividers

Referring to Figure 4, there is shown an all-optical pseudo frequency multiplier built with dedicated wavelength converters (WCs). The all-optical pseudo frequency multiplier is a multi-wavelength system operating on frequencies of the form $f_0 + i\Delta f$, where $i=0, \dots, W-1$, and $W=2^N$. For some integer k , the pseudo frequency multiplier by k maps each frequency f_i to f_{ki} , where $ki \leq W-1$. To implement the mapping, $\left\lceil \frac{W-1}{k} \right\rceil$ converters are used which are dedicated to each of the frequencies f_i , wherein $ki \leq W-1$. The input frequencies are demultiplexed by the wavelength demultiplexer 400 and sent to dedicated wavelength converters (WCs) 401. In the case of f_0 , there is no need for any wavelength converter, since the frequency is not to be changed. The converted frequencies are then multiplexed into the output of the pseudo-multiplier by the multiplexer 402. The dedicated wavelength converters (WCs) 401 may be implemented by cross-gain or cross-phase modulation in semiconductor optical amplifiers.

Similarly, a pseudo frequency divider by k may be implemented that maps each incoming frequency f_{ki} to f_i , with $\left\lceil \frac{W-1}{k} \right\rceil$ converters which are dedicated to each of the channels f_{ki} . For example, referring to Figure 5, there is shown an all-optical pseudo frequency divider built with dedicated wavelength converters (WCs). The design of Figure 5 is essentially obtained by reversing the design of Figure 4. That is, a wavelength demultiplexer 500 separates the different input frequencies, and sends each of them to a dedicated wavelength converter (WC) 501. These wavelength converters (WCs) 501 perform the inverse wavelength conversions of the wavelength converters (WCs) 401 in Figure 4. Then the converted frequencies are sent to a multiplexer 502.

In the designs of both Figure 4 and 5, a number of elementary wavelength converters of the order of $O(W/k)$ are used. Thus, the requirements are difficult to meet when several hundreds of frequencies are in the system.

IV. Logarithmic Converters

For purposes of this detailed description, a multi-wavelength system carrying frequencies of the form $f_0 + i\Delta f$, where $i=0, \dots, W-1$, and $W=2^N$ is assumed. The goal is to implement some frequency mapping Γ , from a subset Φ_{in} into another frequency subset Φ_{out} , where both subsets are included in the spectrum of the multi-wavelength system. If it is assumed that some frequency is always mapped to another larger (or smaller) frequency, each frequency may be converted to its image by performing cascaded frequency translations in amount of the type $2^i \Delta f$ (or

$$\begin{array}{ll} 1.) & \Gamma(f_0)=f_0 \\ 5 \quad 2.) & \Gamma(f_1)=f_2 \\ & 3.) \quad \Gamma(f_2)=f_4 \\ & 4.) \quad \Gamma(f_3)=f_6 \end{array}$$
$$\begin{array}{ll} 1.) & \Gamma(f_0) = f_0 \\ 10 \quad 2.) & \Gamma(f_1) = f_1 + \Delta f \\ & 3.) \quad \Gamma(f_2) = f_2 + 2.\Delta f \\ & 4.) \quad \Gamma(f_3) = f_3 + \Delta f + 2.\Delta f \end{array}$$

$\frac{1}{\Delta f} \cdot (f(f) - f)$ to its binary representation. In the present invention approach, a single frequency shifter by an amount of $\pm 2^i \Delta f$ for each $i=0,1,\dots,N-1$ is provided. This frequency shifter is shared among all the frequencies of Φ_{in} that must be shifted by the corresponding amount. The most generic way to implement this technique uses a central wavelength-selective cross-connect that enables any input frequency f to visit the frequency shifters

Doc #: 170832; V. 1

converters.

A. Generic Log-Converters

Doc # 170832; V. 1

The general design for a logarithmic multistage wavelength converter based on frequency shifters is shown in Figure 6. The overall converter comprises an input slot 601 that receives input frequencies and directs them to a proper frequency shifting module 604 through a central wavelength selective cross-connect (WSXC) 603. An output slot 602 receives the converted frequencies and directs them to an output. For each $i=0, \dots, N-1$, the design uses a single frequency shifting module 604 that provides a frequency translation by $+2^i \Delta f$ (or $-2^i \Delta f$). To implement a frequency mapping with the architecture based on a central wavelength-selective cross-connect, for each input frequency a permutation of the frequency shifters to which it is assigned, must be selected such that the frequencies sent to any frequency shifter are always distinct. When a frequency is not allowed to use the same frequency shifting module more than once, there may be as many as $(2N-2)!$ different possible routes between the input slot and the output slot, for a given input frequency. Overall, an instance of the well-known routing and wavelength assignment problem is obtained (see B. Ramamurthy referenced above). In general, the problem is difficult, and cannot be solved in polynomial time. However, several heuristics exist to compute approximate solutions (see B. Ramamurthy referenced above). In the case of the present invention, these heuristics may enable routes to be found for the different input frequencies, such that no frequency collision occurs in the logarithmic wavelength converter.

Consider the example where $W=8$, $\Phi_{in}=\{f_0, f_1, f_2, f_3\}$,
 $\Phi_{out}=\{f_0, f_4, f_5, f_6\}$ and the mapping Γ such that $\Gamma(f_0)=f_0$,
 $\Gamma(f_1)=f_4$, $\Gamma(f_2)=f_5$, and $\Gamma(f_3)=f_6$. Figure 7 shows a first
routing and wavelength assignment for the mapping Γ . That
is, Figure 7 provides an accurate description of the
routing and wavelength assignment algorithm by using the
notation $z:f_i:f_j$, where f_i is the input frequency, f_j is
the value to which the input frequency has been
converted, up to this stage, and z represents the
sequence of the operation, where the set of operations
include entering or leaving a frequency shifter, leaving
the input slot, as well as entering the output slot. For
example, considering mapping 700, $3:f_1:f_2$ means that after
the fourth operation input frequency f_1 has become f_2 . In
this first routing and wavelength assignment example, a
collision occurs as both the channels corresponding to
the input frequencies f_1 (700) and f_2 (701) enter the
frequency shifter 702, with the same frequency. This
collision may be removed by changing the permutation for
input frequency f_2 , as shown in Figure 8. In Figures 7
and 8, a circuit-switching paradigm is assumed (i.e.,
there is no time-division mechanism to resolve frequency
collisions).

In the generic form described in Figure 6,
logarithmic converters provide many degrees of freedom
regarding the assignment of frequencies to frequency
shifters. However, the hardware complexity of the central
wavelength-selective cross-connect is
 $O(W(2N-1)^2 \log_2(W(2N-1)^2))$. The cost is large when there are
hundreds of frequency channels. Some of the flexibility
of generic log-converters is traded off against reduced

hardware costs when log-converters are considered where the different frequency-shifting modules are assigned distinct priorities or precedence. Thus doing, priority log-converters are obtained.

5

B. Priority Log-Converters

Priority log-converters can implement the subset of routing and wavelength assignment policies where each frequency shifting module is assigned a distinct
10 priority, and each input frequency visits frequency shifting modules in an order respecting the assigned priorities. For a given priority assignment, the generic architecture of Figure 6 is transformed into that of Figure 9. Internally, input optical frequencies are first
15 demultiplexed into two sets named B_0 and T_0 by demultiplexer 901. Input frequencies then go through a succession of N stages, each stage being given by a block with two inputs from the previous stage and giving two outputs into the next stage. When they cross a particular
20 stage, say stage i 906, optical frequencies that enter the stage through B_i 903 are not translated, while the other frequencies that enter the stage through T_i 904 are translated accordingly, in the frequency domain. Before all the frequencies are output, they are sorted into two
25 new input sets for stage $i+1$, which are B_{i+1} 907 and T_{i+1} 905, and so on until stage $N-1$. The outputs of the last stage can be merged into a physical output 909 by an optional multiplexer 908.

In spite of their constraints, priority log-
30 converters are easy to analyze and are appropriate to implement certain increasing mappings. These mappings play key roles in multi-wavelength systems.

V. Constrained Increasing Frequency Mappings

Increasing frequency mappings preserve wavelength ordering from inputs to outputs. They may be classified according to the relative position of input frequencies with respect to their image at the output as follows:

- 1.) When each frequency is mapped to a larger frequency, the increasing mapping is an up-conversion; and
- 2.) When each frequency is mapped to a smaller frequency, the increasing mapping is a down-conversion.

Some increasing mappings do not fall within any of the above-mentioned categories, when in the input frequency set some frequencies are larger than their images while others are smaller.

When an increasing frequency mapping Γ is such that $\Gamma'(f) = \Gamma(f) - f$ is non-decreasing, it can be implemented with priority log-converters. The assignment of priorities to the different frequency shifting modules, as well as the amount of frequency shifting in each of the N consecutive stages, depends on whether the mapping is an up or a down conversion. In both cases, the key idea is to assign the precedence of the frequency shifting modules in an increasing manner with the magnitude of the frequency shift.

A. Constrained Increasing Up-Converter

A frequency mapping Γ is implemented, for mapping a subset Φ_{in} into another frequency subset Φ_{out} , such that Γ is an increasing up-conversion. That is, it satisfies the following properties:

- 1.) Γ is strictly increasing; and
- 2.) For each frequency f in Φ_{in} , $f \leq \Gamma(f)$.

1. Additional Mapping Constraints

Assume that Γ is upperbounded by $f_0 + (W-1)\Delta f$, that the mapping Γ' defined by $\Gamma'(f) = \Gamma(f) - f$ is increasing, and
5 upperbounded by $(W-1)\Delta f$. When $W=8$, an example of such a constrained mapping is Γ from $\Phi_{in} = \{f_0, f_2, f_3\}$ into $\Phi_{out} = \{f_1, f_4, f_7\}$, such that $\Gamma(f_0) = f_1$, $\Gamma(f_2) = f_4$, and $\Gamma(f_3) = f_7$, as is proven by the following observations:
1.) Increasing nature of Γ : $\Gamma(f_3) = f_7 > \Gamma(f_2) = f_4 > \Gamma(f_0) = f_1$;
10 2.) Γ is an up-conversion: $\Gamma(f_0) = f_1 \geq f_0$, $\Gamma(f_2) = f_4 \geq f_2$, and $\Gamma(f_3) = f_7 \geq f_3$; and
3.) $\Gamma'(f) = \Gamma(f) - f$ is increasing:
 $\Gamma(f_3) - f_3 = 4\Delta f > \Gamma(f_2) - f_2 = 2\Delta f > \Gamma(f_0) - f_0 = \Delta f$.

For each frequency f in Φ_{in} , it is noted that
15 $b_{N-1}(f) \dots b_0(f)$ the binary representation of $\frac{1}{\Delta f} \Gamma'(f)$, the most significant digit being $b_{N-1}(f)$.

2. Routing and Wavelength Assignments

An N stage wavelength-conversion technique is used
20 wherein in each stage a frequency shift of the form $2^i \Delta f$ is used, where $i=0, \dots, N-1$. The priorities are increasing based on the frequency shift provided by the stage. In other words, of two stages that provide, the first, a shift $2^i \Delta f$, and the second, a shift $2^j \Delta f$, such that $i < j$,
25 the second has a strictly higher priority. In this scheme, for any frequency that must be shifted by $2^i \Delta f$ and by $2^j \Delta f$, such that $i < j$, there must first be a translation by $2^j \Delta f$ before the translation by $2^i \Delta f$.

The internal details of some stage i of the priority

log-converter of Figure 9 are represented in Figure 10. In this stage i , the processing block 1000 takes two types of inputs, the first type corresponds to the set B_i 1001 and the second type to the set T_i 1002. Each of these sets may carry up to W signals at distinct frequencies. The frequencies which belong to the first input set 1001 are sent to a first $W \times 2W$ wavelength-selective cross-connect (WSXC) 1004. The wavelength-selective cross-connect (WSXC) 1004 switches each frequency to one of two possible output sets, 1006 or 1007. The frequencies which belong to the second input set 1002 are sent to the frequency shifter 1003 and then sent to a second $W \times 2W$ wavelength selective cross-connect (WSXC) 1005. The wavelength-selective cross-connect (WSXC) 1005 switches each frequency to one of two possible output sets, 1008 or 1009. It is important to note that, in stage i , the frequencies coming from the previous stage are processed differently according to the input path taken into the stage:

1.) Input set B_i : Frequencies entering stage i through B_i are sent to the wavelength-selective cross-connect 1004. Frequencies that are to be left unchanged at stage $i+1$ are sent to output set 1006. Each such frequency can be expressed as $f + \Delta f \cdot \sum_{j=N-1-(i-1)}^{N-1} b_j(f) 2^j$, for some f belonging to Φ_m , such that $b_{N-1-i}(f)=0$ and $b_{N-1-(i+1)}(f)=0$. Frequencies that are to be changed in stage $i+1$ are sent to output set 1007. Each such frequency can be expressed as $f + \Delta f \cdot \sum_{j=N-1-(i-1)}^{N-1} b_j(f) 2^j$, for some f belonging to Φ_m , such that $b_{N-1-i}(f)=0$ and $b_{N-1-(i+1)}(f)=1$.

2.) Input set T_i : Frequencies entering stage i through T_i are first up-converted by the frequency shifter 1003 that provides a frequency shift equal to $2^{N-1-i}\Delta f$. The shifted optical frequency channels are then switched to two sets
5 of outputs by the wavelength-selective cross-connect (WSXC) 1005. A frequency is switched to output set 1009 if it is to be left unchanged in stage $i+1$. Each such frequency can be expressed as $f + \Delta f \cdot \sum_{j=N-1-(i-1)}^{N-1} b_j(f) 2^j$, for some f

belonging to Φ_{in} , such that $b_{N-1-i}(f)=1$ and $b_{N-1-(i+1)}(f)=0$. A
10 frequency is switched to output set 1008 if it is to be changed in stage $i+1$. Each such frequency can each be expressed as $f + \Delta f \cdot \sum_{j=N-1-(i-1)}^{N-1} b_j(f) 2^j$, for some f belonging to Φ_{in} , such that $b_{N-1-i}(f)=1$ and $b_{N-1-(i+1)}(f)=1$.

Multiplexer 1010 merges all frequencies to be left
15 unchanged at the next stage (i.e., outputs 1006 and 1009) into input set B_{i+1} 1012 for stage $i+1$, while multiplexer 1011 merges all frequencies to be shifted at the next stage (i.e., output sets 1007 and 1008) into input set T_{i+1} 1013 for stage $i+1$.

20 The above assignment of frequencies in the different stages does produce the right mapping, and it has no frequency collision at any stage.

In Figure 10, the frequency shifter 1003 is used to translate optical frequencies by a fixed amount. Such a
25 frequency shifter may be obtained by cascading two difference frequency wave-mixers driven by different pump frequencies, as shown in Figure 3. Using the design 1000 of Figure 10, the stages where no frequency is shifted may be removed (i.e., any stage i may be removed such

that $b_{N-1-i}(f)=0$ for any input frequency f). When difference frequency wave-mixers are used as basic components, elementary converter requirements of $2N$ are obtained in the worst case (i.e., at least one input frequency is shifted in any stage).

Consider again the example where $W=8$, and Γ maps the set $\Phi_{in}=\{f_0, f_2, f_3\}$ into the set $\Phi_{out}=\{f_1, f_4, f_7\}$, such that $\Gamma(f_0)=f_1$, $\Gamma(f_2)=f_4$, and $\Gamma(f_3)=f_7$. For the different frequencies in Φ_{in} , the binary representations of

$\frac{1}{\Delta f}(\Gamma(f)-f)$ are as follows:

- 1.) $\Gamma(f_0)-f_0=\Delta f:001$
- 2.) $\Gamma(f_2)-f_2=2\Delta f:010$
- 3.) $\Gamma(f_3)-f_3=4\Delta f:100$

In Figure 11, the assignment of the optical frequencies at the consecutive stages of the increasing up-converter are represented for the priority log-converter shown in Figures 9 and 10 when $W=8$, and Γ maps the set $\Phi_{in}=\{f_0, f_2, f_3\}$ into the set $\Phi_{out}=\{f_1, f_4, f_7\}$, such that $\Gamma(f_0)=f_1$, $\Gamma(f_2)=f_4$, and $\Gamma(f_3)=f_7$. That is, each 2-tuple 1101 of the form $f_i:f_j$ indicates that the frequency f_i of the input set Φ_{in} at 1100 has been converted into f_j by the previous stages of the converter. The output 1102 of the converter matches the output frequency set $\Phi_{out}=\{f_1, f_4, f_7\}$. Each of the stages in Figure 11 is internally designed according to the structure of Figure 10.

3. Algorithmic Description

An algorithmic description of the routing and wavelength assignment can be given as follows:

- 1.) Stage -1 (just before stage 0):

For each $f \in \Phi_m$:

If $b_{N-1}(f)=1$: assign f to T_0

Otherwise: assign f to B_0

For $k=0$ to $N-2$: set $b_k^0(f)=b_k(f)$

5 2.) Stage $i \geq 0$:

For each $f \in T_i$:

Set $\tau_i(f)=f+2^{N-1-i}\Delta f$

If $b_{N-1-(i+1)}^i(f)=1$: Assign $\tau_i(f)$ to T_{i+1}

Otherwise: Assign $\tau_i(f)$ to B_{i+1}

10 For each $f \in B_i$:

Set $\tau_i(f)=f$

If $b_{N-1-(i+1)}^i(f)=1$: Assign $\tau_i(f)$ to T_{i+1}

Otherwise: Assign $\tau_i(f)$ to B_{i+1}

For each $f \in T_i \cup B_i$:

15 For $k=0$ to $N-1-(i+2)$: set $b_k^{i+1}(\tau_i(f))=b_k^i(f)$

4. Shift Scaling Technique

It is possible to further reduce the hardware requirements of the converter when for each f belonging to Φ_m , $\Gamma'(f)=\Gamma(f)-f$ is some multiple of $\kappa\Delta f$ where κ is some integer constant characteristic of the mapping Γ . In that case, the number of stages of the log-converter reduces from N to $N-\lfloor \log(\kappa) \rfloor$ in the worst case. For each frequency f in Φ_m , let $c_{N-\lfloor \log(\kappa) \rfloor-1}(f) \dots c_0(f)$ be the binary representation of $\frac{\Gamma'(f)}{\kappa\Delta f}$.

25

Referring to Figure 12, there is shown an alternative internal structure 1200 of some stage i in the priority log-converter of Figure 9 for a constrained increasing up-converter in accordance with the present

invention. That is, Figure 12 shows the internal structure of some stage i of a constrained increasing up-converter when for each f in Φ_{in} , $\Gamma'(f) = \Gamma(f) - f$ is some multiple of $\kappa \Delta f$ where κ is some integer. In the stage i shown in Figure 12, the frequencies are assigned as follows:

1.) Input set B_i : Frequencies entering stage i through B_i are sent to the wavelength-selective cross-connect (WSXC) 1204. Frequencies that are to be left unchanged at stage $i+1$ are sent to output 1206. Each such frequency can be

expressed as $f + \kappa \Delta f \cdot \sum_{j=N-\lfloor \log(\kappa) \rfloor - 1 - (i-1)}^{N-\lfloor \log(\kappa) \rfloor - 1} c_j(f) 2^j$, for some f belonging to

Φ_{in} , such that $c_{N-\lfloor \log(\kappa) \rfloor - 1 - i}(f) = 0$ and $c_{N-\lfloor \log(\kappa) \rfloor - 1 - (i+1)}(f) = 0$.

Frequencies that are to be changed in stage $i+1$ are sent to output 1207. Each such frequency can be expressed as

$f + \kappa \Delta f \cdot \sum_{j=N-\lfloor \log(\kappa) \rfloor - 1 - (i-1)}^{N-\lfloor \log(\kappa) \rfloor - 1} c_j(f) 2^j$, for some frequency f belonging to Φ_{in} ,

such that $c_{N-\lfloor \log(\kappa) \rfloor - 1 - i}(f) = 0$ and $c_{N-\lfloor \log(\kappa) \rfloor - 1 - (i+1)}(f) = 1$.

2.) Input set T_i : Frequencies entering stage i through T_i are first up-converted by wave-mixer 1203 that provides a frequency shift equal to $2^{N-\lfloor \log(\kappa) \rfloor - 1 - i} \kappa \Delta f$. The shifted optical frequency channels are then switched to two types of outputs by the wavelength-selective cross-connect (WSXC) 1205. A frequency is switched to output 1209 if it is to be left unchanged in stage $i+1$. Each such frequency can be

expressed as $f + \kappa \Delta f \cdot \sum_{j=N-\lfloor \log(\kappa) \rfloor - 1 - (i-1)}^{N-\lfloor \log(\kappa) \rfloor - 1} c_j(f) 2^j$, for some frequency f

belonging to Φ_{in} , such that $c_{N-\lfloor \log(\kappa) \rfloor - 1 - i}(f) = 1$ and

$c_{N-\lfloor \log(\kappa) \rfloor - 1 - (i+1)}(f) = 0$. A frequency is switched to output 1208 if it is to be changed in stage $i+1$. Each such frequency can

be expressed as $f + \kappa \Delta f \cdot \sum_{j=N-\lfloor \log(\kappa) \rfloor - 1 - (i-1)}^{N-\lfloor \log(\kappa) \rfloor - 1} c_j(f) 2^j$, for some f belonging to Φ_{in} , such that $c_{N-\lfloor \log(\kappa) \rfloor - 1 - i}(f) = 1$ and $c_{N-\lfloor \log(\kappa) \rfloor - 1 - (i+1)}(f) = 1$.

The design of Figure 12 leads to a constrained increasing up-converter using at most $N - \lfloor \log(\kappa) \rfloor$ elementary frequency shifters, or $2(N - \lfloor \log(\kappa) \rfloor)$ elementary wave-mixers based on difference frequency generation.

Consider the example where $W=16$, and Γ maps the set $\Phi_{in} = \{f_0, f_1, f_2\}$ into the set $\Phi_{out} = \{f_2, f_5, f_8\}$, such that $\Gamma(f_0) = f_2$, $\Gamma(f_1) = f_5$, and $\Gamma(f_2) = f_8$. It is easily checked that Γ satisfies the constraints required to apply the design in accordance with the present invention:

- 1.) Increasing nature of Γ : $\Gamma(f_2) = f_8 > \Gamma(f_1) = f_5 > \Gamma(f_0) = f_2$
- 2.) Γ is an up-conversion: $\Gamma(f_0) = f_2 \geq f_0$, $\Gamma(f_1) = f_5 \geq f_1$, and $\Gamma(f_2) = f_8 \geq f_2$
- 3.) $\Gamma'(f) = \Gamma(f) - f$ is increasing:
 $\Gamma(f_2) - f_2 = 6\Delta f > \Gamma(f_1) - f_1 = 4\Delta f > \Gamma(f_0) - f_0 = 2\Delta f$

For the different frequencies in Φ_{in} , the binary representations of $\frac{1}{\Delta f}(\Gamma(f) - f)$ are as follows:

- 1.) $\Gamma(f_0) - f_0 = 2\Delta f : 010$
- 2.) $\Gamma(f_1) - f_1 = 4\Delta f : 100$
- 3.) $\Gamma(f_2) - f_2 = 6\Delta f : 110$

At this point it should be noted that $\kappa=2$ in the design of Figure 12.

In Figure 13, the assignment of the optical frequencies at the consecutive stages of the increasing up-converter are represented for the priority log-converter shown in Figures 9 and 12 when $W=8$, and Γ maps the set $\Phi_{in} = \{f_0, f_1, f_2\}$ into the set $\Phi_{out} = \{f_2, f_5, f_8\}$, such that

$\Gamma(f_0)=f_2$, $\Gamma(f_1)=f_5$, and $\Gamma(f_2)=f_8$. That is, each 2-tuple 1301 of the form $f_i:f_j$ indicates that the frequency f_i of the input set Φ_{in} at 1300 has been converted into f_j by the previous stages of the converter. The output 1302 of the converter matches the output frequency set $\Phi_{out}=\{f_2, f_5, f_8\}$. Each of the stages in Figure 13 is internally designed according to the structure of Figure 12.

B. Constrained Increasing Down-Converter

10 Consider a frequency mapping Γ , from a subset Φ_{in} into another frequency subset Φ_{out} , such that Γ is an increasing down-conversion. That is, it satisfies the following properties:

- 1.) Γ is increasing
- 15 2.) For each frequency f in Φ_{in} , $\Gamma(f) \leq f$

1. Additional Mapping Constraints

As before, the mapping Γ is further constrained by assuming that it is upperbounded by $f_0+(W-1)\Delta f$, and that the mapping Γ' defined by $\Gamma'(f)=f-\Gamma(f)$ is also increasing, and bounded above by $(W-1)\Delta f$. When $W=8$, an example of such a mapping is Γ from $\Phi_{in}=\{f_1, f_4, f_7\}$ into $\Phi_{out}=\{f_0, f_2, f_3\}$, such that $\Gamma(f_1)=f_0$, $\Gamma(f_4)=f_2$, and $\Gamma(f_7)=f_3$, as is proven by the following equations:

- 25 1.) Increasing nature of Γ : $\Gamma(f_7)=f_3 > \Gamma(f_4)=f_2 > \Gamma(f_1)=f_0$
- 2.) Γ is an down-conversion: $\Gamma(f_1)=f_0 \leq f_1$, $\Gamma(f_4)=f_2 \leq f_4$, and $\Gamma(f_7)=f_3 \leq f_7$
- 3.) $\Gamma'(f)=f-\Gamma(f)$ is increasing:
 $f_7-\Gamma(f_7)=4\Delta f > f_4-\Gamma(f_4)=2\Delta f > f_1-\Gamma(f_1)=\Delta f$

30

2. Routing and Wavelength Assignments

For each frequency f in Φ_{in} , it is noted that $b_{N-1}(f) \dots b_0(f)$ the binary representation of $\frac{1}{\Delta f} \Gamma(f)$. The mapping is implemented by simply reversing the design of the constrained increasing up-converter, as described above. To be precise, N wavelength conversion stages are used. In each stage, a frequency shift of the form $-2^i \Delta f$ is used, where $i=0, \dots, N-1$. The priorities are increasing with the frequency shift provided by the different stages. If there are two stages that provide, the first, a shift $-2^i \Delta f$, and the second, a shift $-2^j \Delta f$, such that $i > j$, the second has a strictly higher precedence. In this scheme, for any frequency channel that must be frequency shifted by $-2^i \Delta f$ and by $-2^j \Delta f$, such that $i > j$, we must first translate it by $-2^j \Delta f$ before translating it by $-2^i \Delta f$. The design that results from this priority assignment is shown in Figure 14. That is, the internal details of some stage i of the priority log-converter of Figure 9 are represented in Figure 14. The description of Figure 14 is similar to that of Figure 10.

In this stage i , the frequency processing block 1400 takes two input sets 1401 and 1402. Each of these sets may carry up to W frequencies. The frequencies which belong to the first input set 1401 are sent to a first $W \times 2W$ wavelength selective cross-connect (WSXC) 1404. The wavelength-selective cross-connect (WSXC) 1404 switches each frequency to one of two possible outputs, 1406 or 1407. The frequencies which belong to the second input set 1402 are first shifted by the wave-mixer 1403 and then sent to a second $W \times 2W$ wavelength selective cross-

5

15

20

25

Doc #: 170832; V. 1

unchanged at the next stage (i.e., outputs 1406 and 1409)
into input set B_{i+1} for stage $i+1$, while multiplexer 1411
merges all frequencies to be shifted at the next stage
(i.e., outputs 1407 and 1408) into input set T_{i+1} for stage
5 $i+1$.

As with the up-counter, the down-converter may be
built with N elementary frequency shifters or with $2N$
elementary difference frequency wave-mixers.

In Figure 15, the assignment of the optical
10 frequencies at the consecutive stages of the increasing
down-converter are represented for the priority log-
converter shown in Figures 9 and 14 when $W=8$, and Γ maps
 $\Phi_{in} = \{f_1, f_4, f_7\}$ into $\Phi_{out} = \{f_0, f_2, f_3\}$, such that $\Gamma(f_1) = f_0$, $\Gamma(f_4) = f_2$,
and $\Gamma(f_7) = f_3$. That is, each 2-tuple 1501 of the form $f_i : f_j$
15 indicates that the frequency f_i of the input set Φ_{in} at
1500 has been converted into f_j by the previous stages of
the converter. The output 1502 of the converter matches
the output frequency set $\Phi_{out} = \{f_1, f_4, f_7\}$. Each of the stages
in Figure 15 is internally designed according to Figure
20 14.

3. Algorithmic Description

An algorithmic description of the routing and
frequency assignments can be given as follows:

25 1.) Stage -1 (just before stage 0):

For each $f \in \Phi_{in}$:

If $b_0(f) = 1$: assign f to T_0

Otherwise: assign f to B_0

For $k=1$ to $N-1$: set $b_k^0(f) = b_k(f)$

30 2.) Stage $i \geq 0$:

For each $f \in T_i$:

Set $\tau_i(f) = f - 2^i \Delta f$

If $b_{(i+1)}^i(f) = 1$: Assign $\tau_i(f)$ to T_{i+1}

Otherwise: Assign $\tau_i(f)$ to B_{i+1}

For each $f \in B_i$:

5 Set $\tau_i(f) = f$

If $b_{(i+1)}^i(f) = 1$: Assign $\tau_i(f)$ to T_{i+1}

Otherwise: Assign $\tau_i(f)$ to B_{i+1}

For each $f \in T_i \cup B_i$:

For $k = (i+2)$ to $N-1$, set $b_k^{i+1}(\tau_i(f)) = b_k^i(f)$

10

4. Shift Scaling Technique

As before, it is also possible to further reduce the hardware requirements of the converter when for each f belonging to Φ_{in} , $\Gamma'(f) = f - \Gamma(f)$ is some multiple of $\kappa \Delta f$ where
15 κ is some integer constant characteristic of the mapping Γ . In that case, the number of stages of the log-converter reduces from N to $N - \lfloor \log(\kappa) \rfloor$. For each frequency f in Φ_{in} , let $c_{N-\lfloor \log(\kappa) \rfloor-1}(f) \dots c_0(f)$ be the binary representation of $\frac{\Gamma'(f)}{\kappa \Delta f}$.

20 Referring to Figure 16, there is shown an alternative internal structure 1600 of some stage i in the priority log-converter of Figure 9 for a constrained increasing down-converter in accordance with the present invention. That is, Figure 16 shows the internal
25 structure of some stage i of a constrained increasing down-converter when for each f in Φ_{in} , $\Gamma'(f) = f - \Gamma(f)$ is some multiple of $\kappa \Delta f$ where κ is some integer.

In the stage i shown in Figure 16, the frequencies are assigned as follows:

- 1.) Input set B_i : Frequencies entering stage i through B_i are sent to the wavelength-selective cross-connect (WSXC) 1604. Frequencies that are to be left unchanged at stage $i+1$ are sent to output 1606. Each such frequency can be expressed as $f - \kappa \Delta f \cdot \sum_{j=0}^{i-1} c_j(f) 2^j$, for some f belonging to Φ_m , such that $c_i(f)=0$ and $c_{(i+1)}(f)=0$. Frequencies that are to be changed in stage $i+1$ are sent to output 1607. Each such frequency can be expressed as $f - \kappa \Delta f \cdot \sum_{j=0}^{i-1} c_j(f) 2^j$, for some f belonging to Φ_m , such that $c_i(f)=0$ and $c_{(i+1)}(f)=1$.
- 2.) Input set T_i : Frequencies entering stage i through T_i are first down-converted by wave-mixer 1603 that provides a frequency shift equal to $-2^i \kappa \Delta f$. The shifted optical frequency channels are then switched to two types of outputs by the wavelength-selective cross-connect (WSXC) 1605. A frequency is switched to output 1609 if it is to be left unchanged in stage $i+1$. Each such frequency can be expressed as $f - \kappa \Delta f \cdot \sum_{j=0}^{i-1} c_j(f) 2^j$, for some f belonging to Φ_m , such that $c_i(f)=1$ and $c_{(i+1)}(f)=0$. A frequency is switched to output 1608 if it is to be changed in stage $i+1$. Each such frequency can each be expressed as $f - \kappa \Delta f \cdot \sum_{j=0}^{i-1} c_j(f) 2^j$, for some f belonging to Φ_m , such that $c_i(f)=1$ and $c_{(i+1)}(f)=1$.

The design of Figure 16 leads to a constrained increasing down-converter using at most $N - \lfloor \log(\kappa) \rfloor$ elementary frequency shifters, or $2(N - \lfloor \log(\kappa) \rfloor)$ elementary wave-mixers based on difference frequency generation.

Consider the example where $W=16$, and Γ maps the set

$\Phi_{in} = \{f_2, f_5, f_8\}$ into the set $\Phi_{out} = \{f_0, f_1, f_2\}$, such that $\Gamma(f_2) = f_0$, $\Gamma(f_5) = f_1$, and $\Gamma(f_8) = f_2$. It is easily checked that Γ satisfies the constraints required to apply the design in accordance with the present invention:

- 1.) Increasing nature of Γ : $\Gamma(f_8) = f_2 > \Gamma(f_5) = f_1 > \Gamma(f_2) = f_0$
- 2.) Γ is a down-conversion: $\Gamma(f_2) = f_0 \leq f_2$, $\Gamma(f_5) = f_1 \leq f_5$, and $\Gamma(f_8) = f_2 \leq f_8$
- 3.) $\Gamma'(f) = -\Gamma(f) + f$ is increasing:
 $f_8 - \Gamma(f_8) = 6\Delta f > f_5 - \Gamma(f_5) = 4\Delta f > f_2 - \Gamma(f_2) = 2\Delta f$

For the different frequencies in Φ_{in} , the binary representations of $\frac{1}{\Delta f}(f - \Gamma(f))$ are as follows:

- 1.) $f_2 - \Gamma(f_2) = 2\Delta f : 010$
- 2.) $f_5 - \Gamma(f_5) = 4\Delta f : 100$
- 3.) $f_8 - \Gamma(f_8) = 6\Delta f : 110$

At this point it should be noted that $\kappa=2$ in the design of Figure 16.

In Figure 17, the assignment of the optical frequencies at the consecutive stages of the increasing down-converter are represented for the priority log-converter shown in Figures 9 and 16 when $W=16$, and Γ maps the set $\Phi_{in} = \{f_2, f_5, f_8\}$ into the set $\Phi_{out} = \{f_0, f_1, f_2\}$, such that $\Gamma(f_2) = f_0$, $\Gamma(f_5) = f_1$, and $\Gamma(f_8) = f_2$. That is, each 2-tuple of the form $f_i : f_j$ indicates that the frequency f_i of the input set Φ_{in} at 1700 has been converted into f_j by the previous stages of the converter. The output of the converter matches the output frequency set $\Phi_{out} = \{f_0, f_1, f_2\}$. Each of the stages in Figure 17 is internally designed according to the structure of Figure 16.

Pseudo-frequency multipliers or dividers can be constructed with monotonic wavelength converters. For example, Figure 18 shows a pseudo frequency doubler constructed in accordance with the present invention design for increasing up-converters. Similarly, Figure 19 shows a pseudo frequency divider constructed in accordance with the present invention design for increasing down-converters. Both circuits have converter requirements of the order of $O(\log_2 W)$.

There are two basic principles underlying the present invention techniques. First, each input frequency is converted by routing it through $O(\log_2 W)$ consecutive stages, where each stage uses wave-mixing to provide a distinct frequency translation by $\pm 2^i \Delta f$, Δf being the frequency spacing between adjacent frequencies. Second, the route of each frequency is selected to avoid frequency collisions at any stage.

Doc #: 170832; V. 1

5

10

15